

FIG. 1

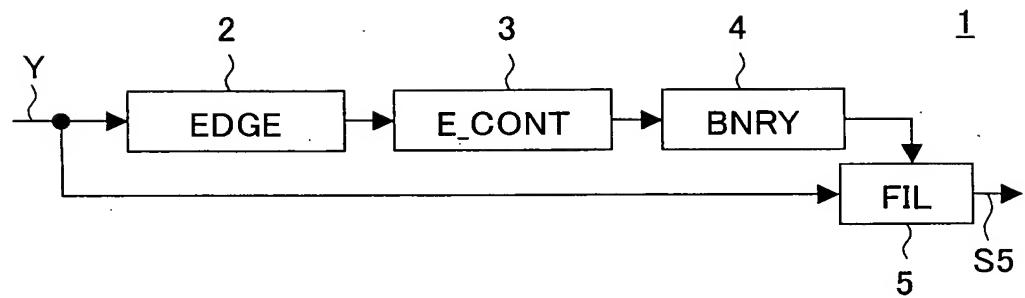


FIG. 2

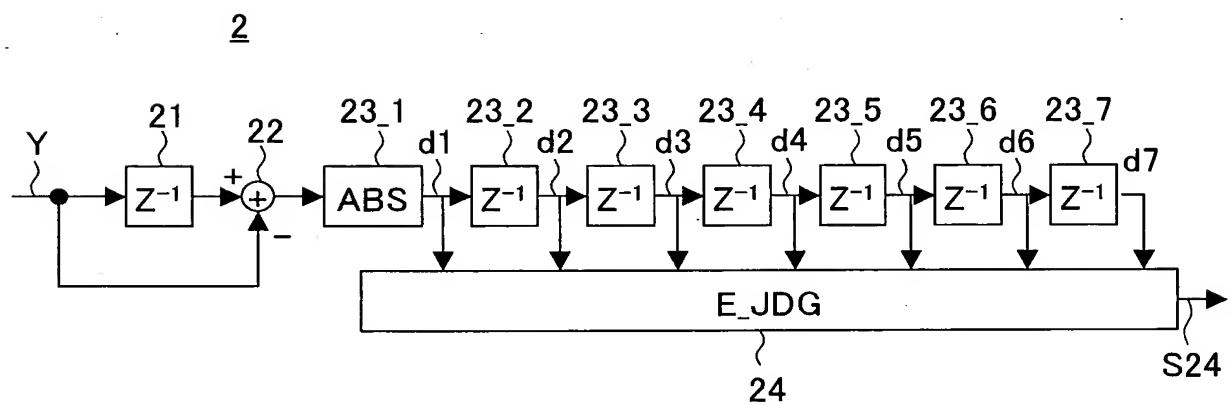


FIG. 3

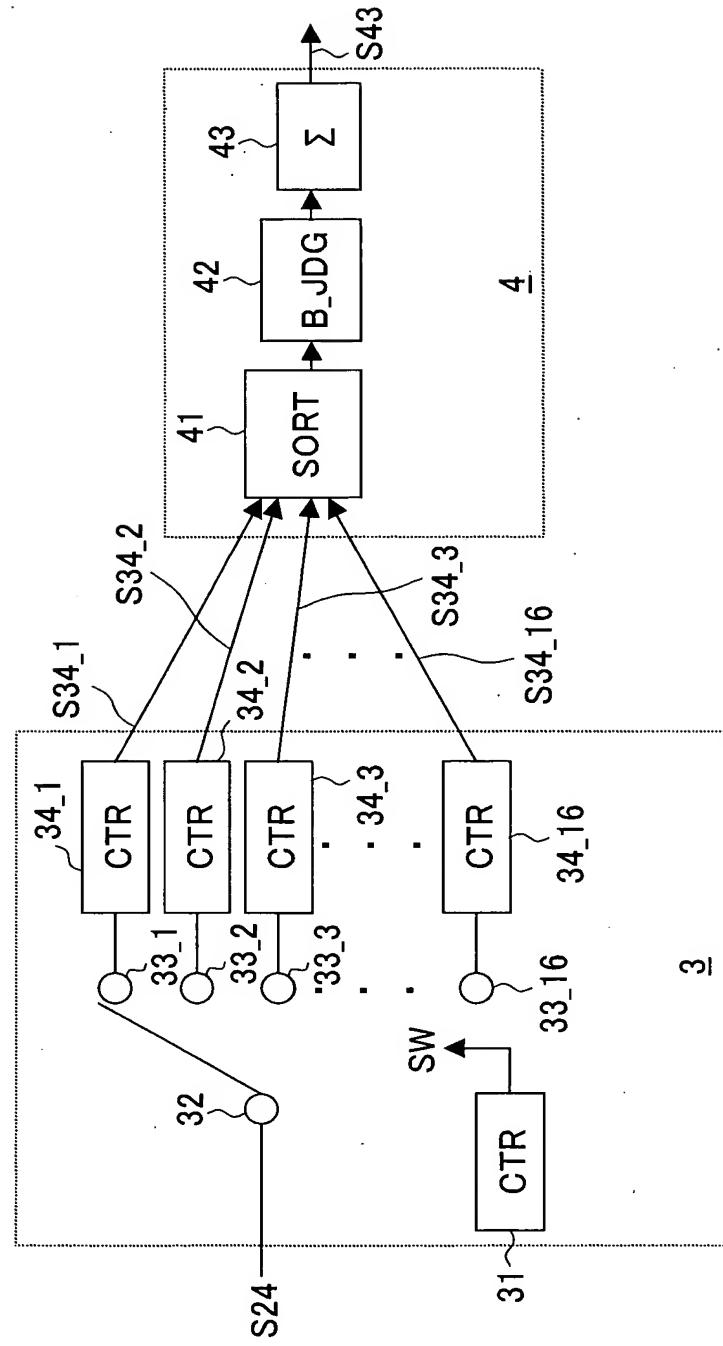


FIG. 4

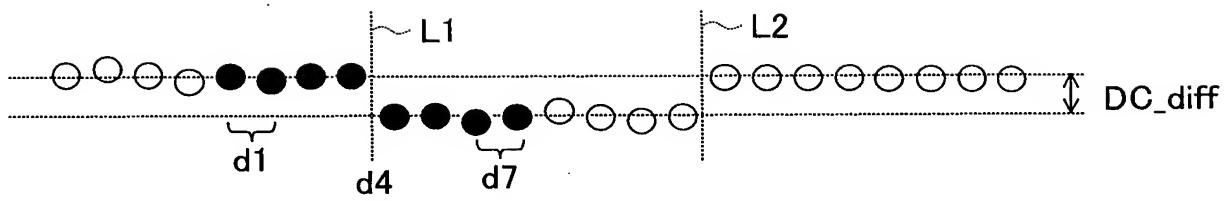


FIG. 5

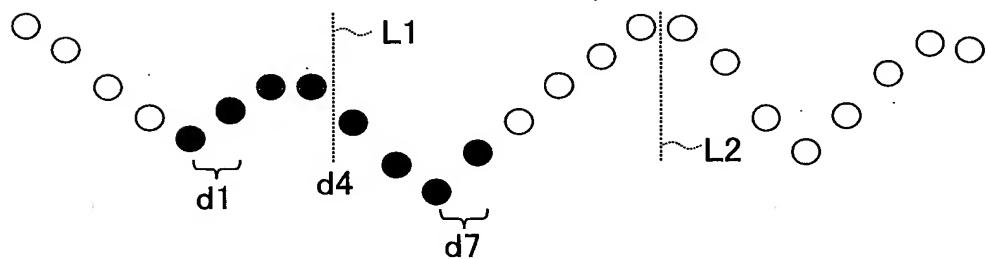


FIG. 6

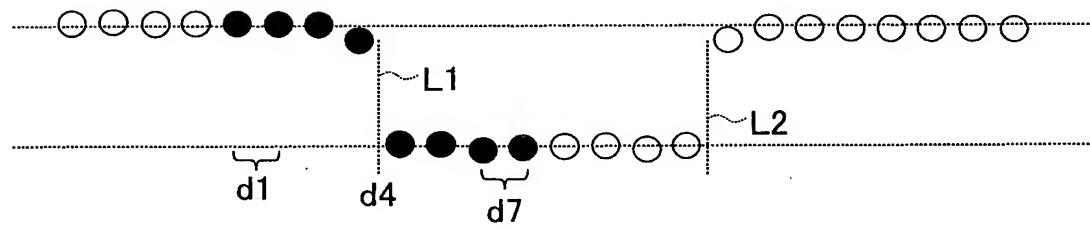


FIG. 7A CLK



FIG. 7B H_SYNC



FIG. 7C H_CTR

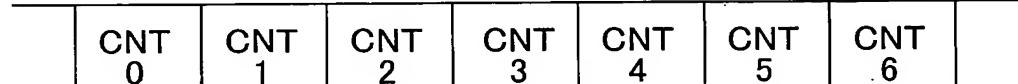


FIG. 8A CLK



FIG. 8B V_SYNC



FIG. 8C E_CTR

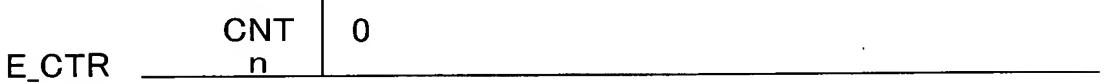


FIG. 8D SORT_R



FIG. 9

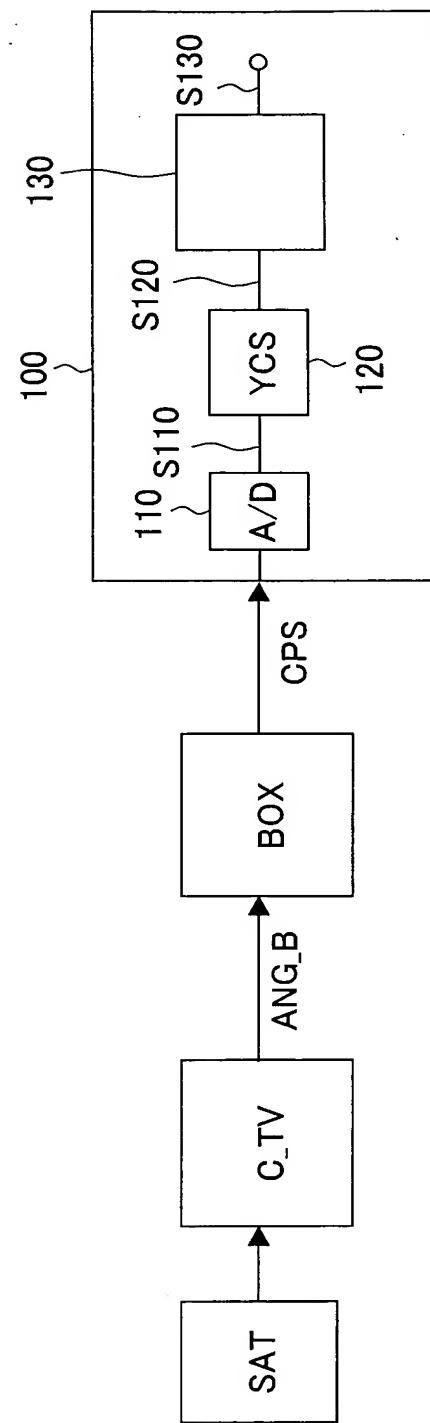
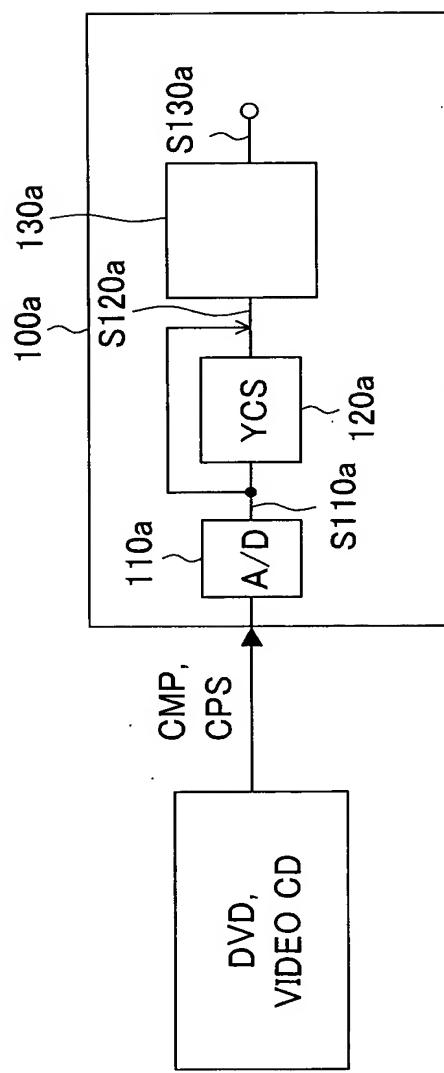


FIG. 10



EXPLANATION OF REFERENCES

- 1... block noise detection apparatus
- 2... edge detection circuit
- 21... delay circuit
- 22... computing circuit
- 23_1... computing unit
- 23_2 to 23_6... delay circuit
- 24... edge determination circuit
- 3... edge count circuit
- 31... horizontal position setting counter
- 32... counter switch
- 33_1 to 33_16... counter contact
- 34_1 to 34_16... edge time counter
- 4... boundary identification circuit
- 41... counter value sort unit
- 42... block boundary determination unit
- 43... time integration unit
- 5... filter
- 110, 110a... video signal processing apparatus
- 110, 110a... A/D converter
- 120, 120a... YC separator
- 130, 130a... block noise detector